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X-1545 US
10/777,327

PATENT
Conf. No.: 4920

IN THE UNITED STATES PATENT OFFICE

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Applicants: Nigel G. Herron et al.
Assignee: Xilinx, Inc.
Title: Testing a Programmable Logic Device with Embedded Fixed Logic Using a Scan Chain
Patent No.: 7,080,300 B1 Issued: 07-16-2006
Serial No.: 10/777,327 File Date: 02-12-04
Examiner: David Ton Art Unit: 2133
Docket No.: X-1545 US Conf. No.: 4920

VIA FACSIMILE Fax: 571-273-0025

3 pages

Attn: Petitions
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450

PETITION UNDER 37 CFR 3.81(b) REQUESTING A
CERTIFICATE OF CORRECTION

Dear Sir:

Applicants respectfully request that the above patent be corrected to state the names of the assignees. Furthermore, enclosed is a Certificate of Correction under §1.323 which Applicants respectfully request be entered with respect to the above patent. The joint Assignee was inadvertently left off the Issue Fee Transmittal at the time of paying the Issue Fee, and the omission only recently came to Applicants' attention.

This was an error in clerical nature and does not constitute new matter or require reexamination.

The Assignment for the above-referenced patent was submitted for recordation as set forth in §3.11 on February 12, 2004 before the issuance of the patent on July 16, 2006. See Reel/Frame 014989/0882 (IBM) and 016284/0978 (Xilinx)

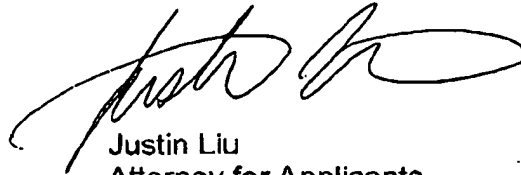
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02 FC:1811 100.00 DA

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The examiner is hereby authorized to charge the fee of \$100.00 under 37 CFR §1.20(a) for the Certificate of Correction and the processing fee of \$130.00 as set forth in §1.17(i) and charge any additional fees or credit any overpayment which may be required to Deposit Account No. 24-0040.

Respectfully submitted,



Justin Liu
Attorney for Applicants
Reg. No. 51,959

I hereby certify that this correspondence is being sent via Facsimile to 571-273-0025 addressed to: Petitions, Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450, on December 11, 2008.

Pat Tompkins
Name

/Pat Tompkins/
Signature